



**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Chikao IKENAGA et al.

Application No.: 10/781,947

Docket No.: 123784

Filed: February 20, 2004

For: SEMICONDUCTOR DEVICE FABRICATING APPARATUS AND  
SEMICONDUCTOR DEVICE FABRICATING METHOD

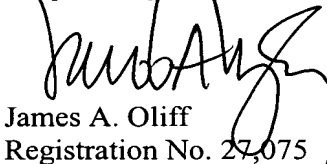
**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

  
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Date: August 18, 2005

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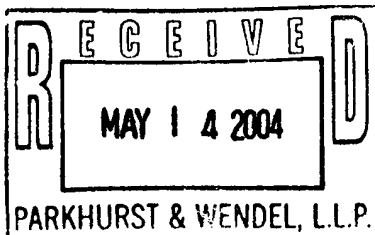


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/781,947	02/20/2004	2811	900	DAIN:763	7	12	2

6160  
PARKHURST & WENDEL, L.L.P.  
1421 PRINCE STREET  
SUITE 210  
ALEXANDRIA, VA 22314-2805



CONFIRMATION NO. 9701

UPDATED FILING RECEIPT



\*0C000000012651637\*

Date Mailed: 05/13/2004

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## Applicant(s)

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## Assignment For Published Patent Application

Dai Nippon Printing Co., Ltd., Shinjuku-Ku, JAPAN;  
Nitto Denko Corporation, Ibaraki-Shi, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2003-43680 02/21/2003

If Required, Foreign Filing License Granted: 05/13/2004

Projected Publication Date: 08/26/2004

Non-Publication Request: No

**Early Publication Request: No**

**Title**

Semiconductor device fabricating apparatus and semiconductor device fabricating method

**Preliminary Class**

257

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